

A Novel Compact Rectangular Microstrip Patch Antenna with a Superstrate Element for 2.4GHz WLAN Applications

Goker Sener Department of Electrical-Electronics Engineering, Cankaya University, Ankara, Turkey

Abstract

This paper presents a new compact rectangular microstrip patch antenna with a superstrate element. This antenna operates at 2.4 GHz TM_{01} fundamental mode, which is suitable for WLAN applications. The patch area is reduced by 50% by placing three rectangular slots on the ground plane. In order to compensate for the decreased gain due to the size reduction, a high permittivity superstrate is used with 4mm thickness and 5mm height from the antenna surface. The proposed antenna offers the advantage of occupying half the area of the non-modified rectangular patch while it possesses the same broadside gain of 6-7dB. The trade-off is the additional antenna height due to the placement of the superstrate element.

Key words: Microstrip antennas, Compact antennas, WLAN applications.

1. Introduction

Microstrip patch antennas are created by placing metal geometries (patches) on the upper surface of a dielectric structure (substrate) whose bottom surface is grounded. These metal patches are usually in rectangular or circular geometry, and they are available at every frequency between 100 MHz and 100 GHz. General advantages of microstrip antennas are their light weight, low cost, low cross-section profile and compatibility with planar structures. The general drawbacks are their narrow bandwidth and moderate gain. Overall, they have 1-2% impedance bandwidth and the highest gain of about 6 dB; Therefore, they are used more often in aviation and mobile applications [1].

Microstrip patch antennas are frequently used in 2.4-2.5 GHz ISM (Industrial, Scientific and Medical) applications. For example, in [2], the bandwidth and front-back gain ratio of a microstrip patch antenna are increased by using electromagnetic band gap (EBG) structures within the substrate layer for WLAN applications at 2.4 GHz. Also in [3], a compact microstrip patch antenna is developed for WLAN (2.4 GHz), WiMAX (2.3 GHz, 2.5 GHz, 3.5 GHz) and Wi-Fi (2.4 GHz) applications.

Miniaturization of patch antennas have important advantages. Today's wireless communication systems require small antennas in mobile phones and similar units. In addition, because array antennas can contain tens or even hundreds of patches, the dimensions of each patch significantly affect the size of the overall antenna. The following methods are generally used to reduce the size of the microstrip patch antennas [4]: First, the permittivity of the dielectric layer can be increased. However, this bounds the electric field tightly within the dielectric substrate, thereby shortening the area that radiates from the edges. Second, the patch can be shorted to ground from one of its

*Corresponding author: Address: Faculty of Engineering, Department of Civil Engineering Sakarya University, 54187, Sakarya TURKEY. E-mail address: caglar@sakarya.edu.tr, Phone: +902642955752

edges. This technique also reduces the patch dimension; however, the antenna radiates only from one edge, yielding poor radiation efficiency. The third technique is to open meandering slots on the patch surface, or on the ground plane. In this technique, the antenna operates at the same frequency with smaller dimensions, because the surface currents travel further distance due to meandering lines. The disadvantage of this method is that the gain of the antenna decreases as a result of the radiation from the slots [4]. An example of this technique is the microstrip patch in [5] operated at 2.4 GHz having 3 slots on the ground plane of the antenna, establishing the antenna miniaturization by 50%.

One of the methods to compensate for the gain loss in compact patch antennas is to use a di-electric layer (superstrate) at a certain height above the surface of the antenna [1,6]. This di-electric layer acts as a reflector lens that collects the radiation in the direction perpendicular to the antenna surface and increases the gain. Therefore, as the electric permittivity and thickness of this upper layer increases, the gain of the antenna increases proportionally. In literature, there are many examples of superstrate usage for compact patch antennas. For example, in [7], ground shorted compact rectangular patch is designed to operate at 1.84 GHz having a superstrate on top of its substrate with electric permittivity constant ϵ_r =79 and thickness h=3.05 mm. The antenna gain of 10.8 dB is attained. Again, in [8], An FR4 di-electric superstrate is placed on top of a compact micro-strip antenna resulting in a gain of 15.8 dB at 5.8 GHz.

The patch antenna presented in this article is formed by modifying a rectangular microstrip antenna (RMSA) by opening meandering slots on the ground plane. Furthermore, a superstrate with high electric permittivity is used to compensate for the gain loss. The proposed compact patch antenna is compared with a plain rectangular patch antenna using HFSS computer simulation. The results show that the proposed antenna has 50% less patch surface than the standard RMSA while retaining the broadside gain of 7.24 dB, which is slightly greater than the plain RMSA gain of 6.67 dB. The impedance bandwidth results are 7.5% versus 1% for the proposed and the plain RMSA respectively. Thus, both the gain and the bandwidth are improved for the proposed design despite its reduced size. On the other hand, the trade-off is the increased antenna height by 9mm.

2. The Compact RMSA with a Superstrate Element

Based on the technique called the "Cavity Model", the resonant frequency of a plain RMSA at TM_{01} fundamental mode can be obtained from the following formula [1,6,9]

$$f = 2.4GHz = \frac{1}{2L\sqrt{\mu\epsilon}} \tag{1}$$

where L is the patch length to be determined, and μ and ϵ are the electrical properties of the substrate. Solving (1) for $\mu = \mu_0$ and $\epsilon = \epsilon_r \epsilon_0 = 2.1 \epsilon_0$, L=40 mm is obtained; however, in reality, this value is slightly larger because of the fringing fields that exist along the edges of the patch. If we take the height of the antenna as h=1.6 mm, then the width (W) of the patch can be taken as W=30 mm as it must satisfy the criteria L > W > h [4].

Table 1. Design parameters and their values in mm's.

dH^*	€ _{Sub} *	ϵ_{Sup}^{*}	patchX	patchY	subX	subY	dX	dY	dsX	dsY*	dsH	dsZ
1.6	2.1	12	40.43	30	100	60	20	30	1	26.2	5	4

 ϵ_{Sub}^* and ϵ_{Sup}^* are the relative permittivity values of the substrate (RT/duroid 5880) and superstrate layers respectively. dsY^{*}=26.2 mm is the total slot length where 1.2 mm is outside, 25 mm is inside the patch area. dH^{*} is the substrate height.



Figure 1. The proposed compact RMSA and the plain RMSA in (a) 3D view, (b) top view.

The proposed compact antenna and the plain RMSA are shown in Figure 1. The dimensions of the antennas are presented in Table 1. In the proposed design, the patch area is reduced by 50% by placing 3 meandering slots on the ground plane. The locations and the widths of the slots are determined based on techniques explained in [5]. The length of the slots is adjusted until the desired frequency of 2.4 GHz is attained at half the patch length.

The superstrate layer is made of silicon, and is placed 5 mm above the antenna surface. It has a thickness of 4 mm, and has the same azimuthal dimensions as the antenna substrate. The antenna is fed by a 50Ω microstrip transmission line, which is matched to the patch by a quarter wave transformer line.

The impedance bandwidth of the proposed antenna is shown in Figure 2. The graph shows that the plain antenna has an impedance bandwidth of 1%, which is typical for an RMSA. On the other hand, the proposed antenna has an impedance bandwidth of 7.5%, which is a significant improvement, mostly due to the presence of the superstrate.

The principle E-plane ($\varphi=0^{0}$ plane or xz-plane) radiation pattern of the antenna is shown in Figure 3. The co-polar (co-pol.) and cross-polar (x-pol.) power gains are obtained as (7.24 dB, -45 dB) and (6.65 dB, -45 dB) for the proposed RMSA and the plain RMSA respectively. The front-to-back ratio is 13.5 dB for the proposed antenna, and 37 dB for the plain RMSA. The reason for this reduction is the back radiation from the slots on the ground plane. Placing slots on the front patch would compensate for the front to back ratio loss; however, this would increase the cross-polar radiation, leading to severe radiation impurity.







Figure 3. E-Plane radiation pattern for co-pol. (E_{θ}) and x-pol. (E_{ϕ}) electric field components.

3. Conclusion

A compact patch antenna is designed to operate at 2.4 GHz ISM band with 50% less patch area than the plain RMSA. In order to compensate for the gain loss, a silicon superstrate layer is placed 5 mm above the antenna surface at 4 mm thickness. Thus, the gain of the antenna is increased to 7.24 dB for the co-polar field in the broadside direction. The cross polar power gain of the antenna remains the same as the plain RMSA at -45 dB. The impedance bandwidth of the antenna is increased from 1% to 7.5%, while the front-rear area intensity ratio is decreased to 13.5 dB from

37 dB of the plain RMSA. Thus, the proposed antenna is suitable for ISM band applications where high gain, wide bandwidth and small antenna surface are desired.

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